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(54) Equaliser for radio receiver.

(57) A GMSK radio receiver for operation in
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are correlated with a plurality of different possible signals
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IMPROVEMENTS IN O

ATING TO EQUALISERS

This invention relates to equalisers for radio receivers of the kind destined for operation in the presence of strong multipath interference.

Mobile telephones, for example, include receivers of which the receiver receives transmission paths. The transmission signals are usually frequency modulated signals and the carrier frequency is of length equal to a delay equivalent to up to four paths. The multipath interference can produce signal paths of original data used to modulate the carrier wave. In particular, digital data or voice communications sphere are frequently of the Gaussian Minimum Shift Keying (GMSK) type. The receiver includes a baseband frequency converter which demodulates the received transmissions to provide I and Q baseband signals distorted by the multipath interference present.

The data to be transmitted is arranged in packets and each packet is transmitted with a header including an address and a predetermined data sequence. This predetermined data sequence is employed for channel distortion due to the multi-path interference.

If the channel impulse is determined, subcarriers are transmitted data bits, depending on the preceeding bits can be interpreted on a probability basis.

It is possible using a Viterbi algorithm to correct the received GMSK signals with all possible signals and to select, on the basis of probability, the most likely signal. This most likely signal is assumed to be the original signal.

An apparatus to effect a complete compensation would be complex, expensive, and of size and power requirements that it would not constitute a commercial solution of the problem of multipath interference; particularly for receivers such as those used in mobile telephones.

It is an object of the present invention to provide an equaliser for a radio receiver wherein the aforesaid disadvantages are overcome.

According to the present invention, there is provided an equaliser, for a radio receiver, comprising a baseband frequency converter for producing, from a received signal, digital samples at baseband, means for producing an estimate of channel distortion in the received signal, a store in which are stored signal sets, means for applying the distortion estimate to the stored signal sets, metric generating means for generating metrics of the signal sets, and a processor for processing the generated metrics in accordance with a Viterbi algorithm to determine the most probable value of the received signal.

The invention will be described further, by way of example, with reference to the accompanying drawings, in which:-

Figure 1 is a block schematic diagram of a receiver including an equaliser according to the present invention;

Figure 2 is a block schematic diagram of a metric generator forming part of the equaliser of Figure 1;

Figure 3 is a block schematic diagram of a metric calculator of the metric generator of Figure 2;

Figure 4 is a block schematic diagram of a metric selector of the metric generator of Figure 2; and

Figure 5 is an 8-state trellis diagram.

In the example hereinafter described, it is assumed that voice or data signals are transmitted in packets by a known system using Gaussian Minimum Shift Keying (GMSK). In such a system, the information to be transmitted is converted to digital form and is digitized information are thereby modified in a manner dependent upon one or more preceding bits. The so modified signals may then be encoded (or modulated) and used to frequency modulate a carrier wave. The modulated carrier wave having, as its modulation, the digitized, modified encoded information, is transmitted.

As stated above, the information is transmitted in packets and each packet comprises a header including a predetermined bit sequence and a data sequence.

A base station transmits the modulated signal to mobile receivers (transceivers) such as mobile telephones. Where the transmission is effected in a city environment, reflections from buildings, for example, causes multipath interference so that the received signal may include echoes (caused by different path lengths of reflected signals) along with the wanted signal. The different path lengths may distort each period $4T$ equivalent to the transmission time of for the signal (where T is the bit interval).

The invention is directed to extracting the wanted signal from the distorted signal caused by the multipath interference.

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The invention is directed to extracting the wanted signal from the distorted signal caused by the multipath interference.

Referring now to Figure 1, a distorted signal is passed from the aerial to baseband frequency converter 10.

The converter 10 outputs I and Q baseband signals.

In each packet, the first part of the sequence. The baseband GMSK signals are converted to digital values in an A to D converter 12. The initial part of the GMSK signal sequence, the channel impulse response estimator 14.

The distortion of this known predetermined sequence enables a "channel impulse response" to be estimated. The channel impulse response is affected the transmitted information. The signals, from a store 18, in a convolutor 16 selector 20, are fed to a metric generator 22. The metrics so generated are also fed. The metrics are generated by algorithms (as indicated in the trellis diagram of Figure 3) to produce the most probable sequence, as detected data, at its output and, in dependence on the probable sequence selected, an output of the "last bit" back to the selector 20.

A path store is provided in which previous bits, selection of corresponding signal sets and appropriate metrics in the next data bit into the store 18 only holds a reduced set of sequences can be reduced to 32. The averaging technique is described below.

By using an averaging technique, in a path store is provided in which previous bits, selection of corresponding signal sets and appropriate metrics in the next data bit into the store 18 only holds a reduced set of sequences can be reduced to 32. The averaging technique is described below.

At the receiver, a set of signals would be stored corresponding to all possible combinations of a 7-bit sequence that, when modulated, will produce one of the waveforms. Denoting the dependence of the modulated waveform on the bits $a_0, a_1, a_2, a_3, a_4, a_5, a_6$ (each a_i taking ± 1), then the dual bits in a given time interval is thus:-

30

35

0 →
-T
-2T
-3T
-4T

bits
a_0, a_{-1}, a_{-2}
a_{-1}, a_{-2}, a_{-3}
a_{-2}, a_{-3}, a_{-4}
a_{-3}, a_{-4}, a_{-5}
a_{-4}, a_{-5}, a_{-6}

Due to the nature of the filtering applied to the transmitted waveform the value of the transmitted waveform that is averaged is used to produce a signal that sufficiently approximate the true signal set.

To form the approximate waveform, for $n = 0$

45

$$s(t_1 a) = g_0(t)$$

50

where $g_0(t) = \langle \exp(j2\pi h a_0 q(t)) \rangle_{a_0}$
 ($\langle \rangle_{a_0}$ denotes an averaging over a_0)
 ϕ' is the redefined phase state

55

d signal is passed from the aerial to baseband frequency converter 10.

storted GMSK signals.

d received signal corresponds to the predetermined bit sequence. The initial part of the GMSK signal sequence, the channel impulse response estimator 14.

it sequence enables a "channel impulse response" to be measured of the distortion, caused by echoes, which has estimate may be applied to each of a series of stored most appropriate stored signal sequences, selected in a which, on a bit-by-bit basis, the baseband GMSK signal is the Viterbi processor 24 which applies known Viterbi algorithm (as indicated in the trellis diagram of Figure 3) to produce the most probable sequence, as detected data, at its output and, in dependence on the probable sequence selected, an output of the "last bit" back to the selector 20.

bits are stored. Depending on the value of these preceding bits, selection of corresponding signal sets and appropriate metrics in the next data bit into the store 18 only holds a reduced set of sequences can be reduced to 32. The averaging technique is described below.

sible data sequences. A full set requires 128 combinations. In accordance with the present invention, this number of stored sequences is reduced to 32 by using an averaging technique to reduce the number of stored sequences is described below.

lly be stored corresponding to all possible combinations of a 7-bit sequence that, when modulated, will produce one of the waveforms. Denoting the dependence of the modulated waveform on the bits $a_0, a_1, a_2, a_3, a_4, a_5, a_6$ (each a_i taking ± 1), then the dual bits in a given time interval is thus:-

or to modulation, the bits a_0 and a_{-6} have less effect upon the signals that sufficiently approximate the true signal set. and $n = -4$, $s(t, a)$ is modified in such a manner:-

$$s(t, a) = \exp(j2\pi h \sum_{i=-5}^{-1} a_i q(t-iT)) \exp(j\phi')$$

$$\text{Im}\{c(t, \underline{a})\} = \text{Im}\{s(t, \underline{a})\} \otimes \text{Re}\{h(t)\} + \text{Re}\{s(t, \underline{a})\}$$

Further the multiplication by 0.5 in the if the store modulated data has the following

$$s(t, \underline{a}) = (1/\sqrt{2}) \exp(j\phi(t, \underline{a})) \quad (1.6)$$

The important point here is the multiplication

The sixteen selected signal sets are fed to the metric generator 22 together with the same metrics which are used in the processor 24

In calculating the metrics, it is only necessary for the correlation process reduces to a single generation process is to require the real and imaginary parts of the signal set, thus four correlators are used. The phase takes the values $0, \pi/2, \pi, 3\pi/2$, it is obvious which pair depend upon the phase state accumulated phase as 0 the process described

$$\Gamma(\underline{a}) = \cos(\theta) \left(\int_{nT}^{(n+1)T} \text{Re}\{c(t, \underline{a})\} \right.$$

$$\left. + \sin(\theta) \left(\int_{nT}^{(n+1)T} \text{Re}\{c(t, \underline{a})\} \right. \right.$$

$$\left. - 0.5 \int_{nT}^{(n+1)T} |c(t, \underline{a})|^2 dt \right.$$

$$\theta = \text{Mod}_{2\pi} \left(\pi/2 \sum_{i=-\infty}^{m-4} a_i \right)$$

As stated hereafter in the description of the generation of the metrics depends upon the path store. The metric is determined from the path store. The metric is determined from the path store. The metric is determined from the path store.

The metrics generated in the metric generator are fed to the processor 24 which determines probability of each state.

Given the sixteen signals, the number of states in the trellis is 64, which includes a set of phase states for each transmitted signal. This accumulated phase can take one of four values (when reduced modulo 2π): $0, \pi/2, \pi, 3\pi/2$. The reduced state equaliser described does not incorporate the phase states. Instead, for each state in the trellis, the accumulated phase is calculated based on the contents of the path store described above. To reduce further the number of states, a subset of the sequence \underline{a} (sequence \underline{b}) is used in the Viterbi processing. The sequence \underline{b} has a length of four symbols and consequently there are 8 states in the trellis diagram as shown in Figure 5.

With the phase states removed and the length of the sequence \underline{a} a 16 state equaliser is used. The following task: a selection procedure is used to select the metric constant. This involves taking

$$h(t) \quad (1.3)$$

of the signal set (as in equation 1.1) need not be done

by $1/\sqrt{2}$.

The signal selector 20 to the metric calculators 23 of the baseband GMSK signals. The generator 22 produces the metrics which are used in the processor 24

to use one sample/symbol. Consequently the FIR filtering is required. The effect of the accumulated phase in the metric generation process is to be correlated with both the real and imaginary parts of the signal set, thus four correlators are used. The phase takes the values $0, \pi/2, \pi, 3\pi/2$, it is obvious which pair depend upon the phase state accumulated phase as 0 the process described above is readily seen.

$$\left. \int_{nT}^{(n+1)T} \text{Im}\{c(t, \underline{a})\} Q(t) dt \right.$$

$$\left. \int_{nT}^{(n+1)T} \text{Im}\{c(t, \underline{a})\} I(t) dt \right.$$

(3.1)

(3.2)

processor 24, the selection of which signals to use in the metric generation process for a given sequence \underline{b} is shown in Figures 5 and 6.

The metrics generated in the metric generator are fed to the processor 24 which determines probability of each state.

Given the sixteen signals, the number of states in the trellis is 64, which includes a set of phase states for each transmitted signal. This accumulated phase can take one of four values (when reduced modulo 2π): $0, \pi/2, \pi, 3\pi/2$. The reduced state equaliser described does not incorporate the phase states. Instead, for each state in the trellis, the accumulated phase is calculated based on the contents of the path store described above. To reduce further the number of states, a subset of the sequence \underline{a} (sequence \underline{b}) is used in the Viterbi processing. The sequence \underline{b} has a length of four symbols and consequently there are 8 states in the trellis diagram as shown in Figure 5.

With the phase states removed and the length of the sequence \underline{a} a 16 state equaliser is used. The following task: a selection procedure is used to select the metric constant. This involves taking

and selecting the sequence with the largest metric. The largest of the surviving metrics forms the basis for the next number of states to 8, the following modification is made: sequences that differ only in the symbol a_{m-4} , a_{m-3} are involved in the maximisation of the metric. In the 16 state equaliser, at each state in the trellis, there are two possible transitions. For each transition, a metric is generated in the next symbol interval depending upon the symbol a_{m-3} . To determine the most probable value of the received signal, the content of the path store is updated with the symbol a_{m-3} .

The values of the processed metrics are averaged to produce the most probable signal. This is the output for further voice and/or data communication.

Because averaging is used to reduce all path metrics, the contents of the path store to control signal processing are reduced from 7-bit values to 5-bit values, and because of the use of a reduced state processor is possible.

Claims

1. An equaliser, for a radio receiver, comprising: means for producing digital samples at baseband, means for providing a state path store in which are stored signal sets, means for generating metrics, means for selecting a sub-set of the stored signal sets with the digital samples, and a processor for processing the generated metrics to determine the most probable value of the received signal.
2. An equaliser as claimed in claim 1 which includes means for comparing a known data sequence and for deriving the estimate therefrom.
3. An equaliser as claimed in claim 1 wherein the state path store represents averaged values of all possible 16 states.
4. An equaliser as claimed in claim 3 including sixteen of stored signal sets which differ in dependence upon the contents of the state path store.
5. An equaliser as claimed in any preceding claim wherein the state path store is reduced to 8 states upon the symbol a_{m-3} , to reduce the equaliser to an 8-state equaliser.
6. An equaliser, for a radio receiver, substantially as hereinbefore described with reference to the accompanying drawings.

This is performed over all such combinations, and the decision about the symbol a_{m-N+1} . To reduce the complexity of the procedure outlined above is made: instead of taking sequences of length 4 symbols, that differ in the symbol a_{m-4} , a_{m-3} are involved in the maximisation of the metric. In the 16 state equaliser, at each state in the trellis, there are two possible transitions. For each transition, a metric is generated in the next symbol interval depending upon the symbol a_{m-3} . To determine the most probable value of the received signal, the content of the path store is updated with the symbol a_{m-3} .

The values of the processed metrics are averaged to produce the most probable signal. This is the output for further voice and/or data communication.

Because averaging is used to reduce all path metrics, the contents of the path store to control signal processing are reduced from 7-bit values to 5-bit values, and because of the use of a reduced state processor is possible.

g a converter for producing, from a received signal, an estimate of channel distortion in the received signal, applying the distortion estimate to the stored signal sets, selecting modulated signal sets with the digital samples and processing the selected signal sets in accordance with a Viterbi algorithm to determine the most probable value of the received signal.

g means for producing an estimate of channel distortion in the received signal, received as a distorted signal, with the known data sequence and for deriving the estimate therefrom.

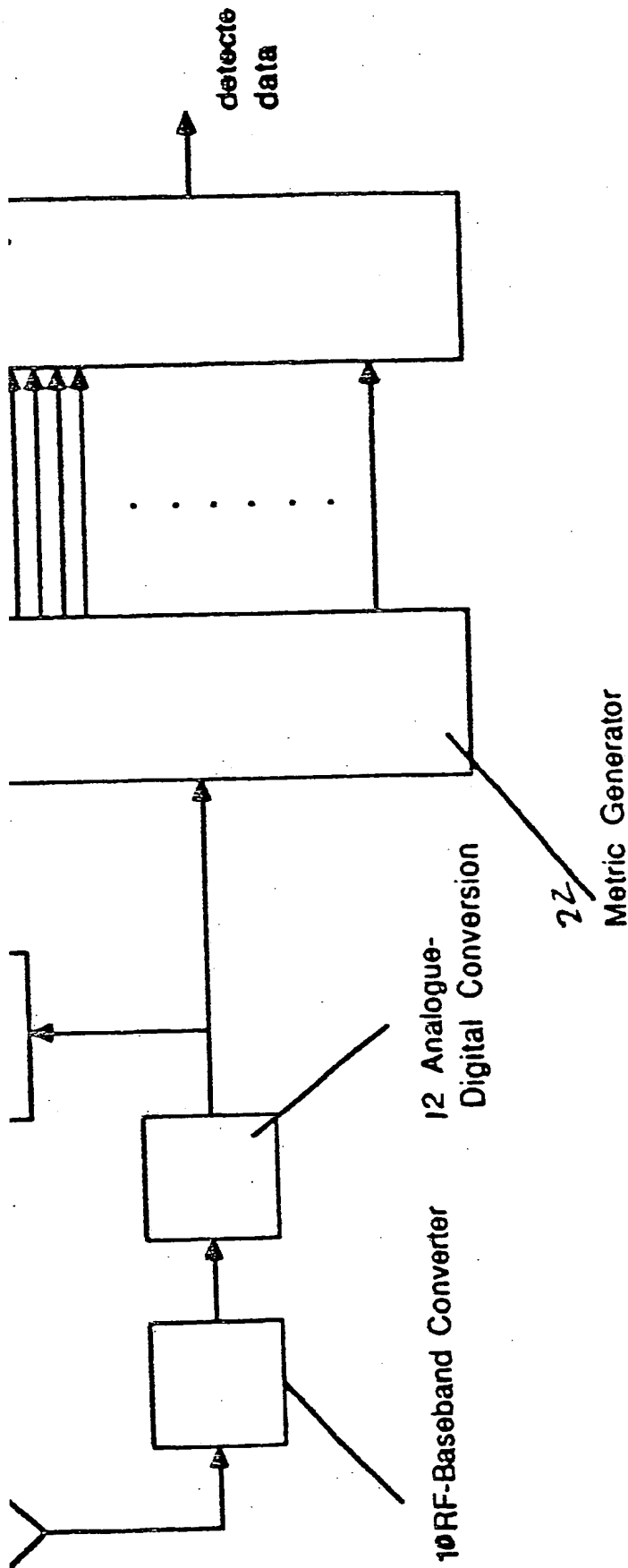
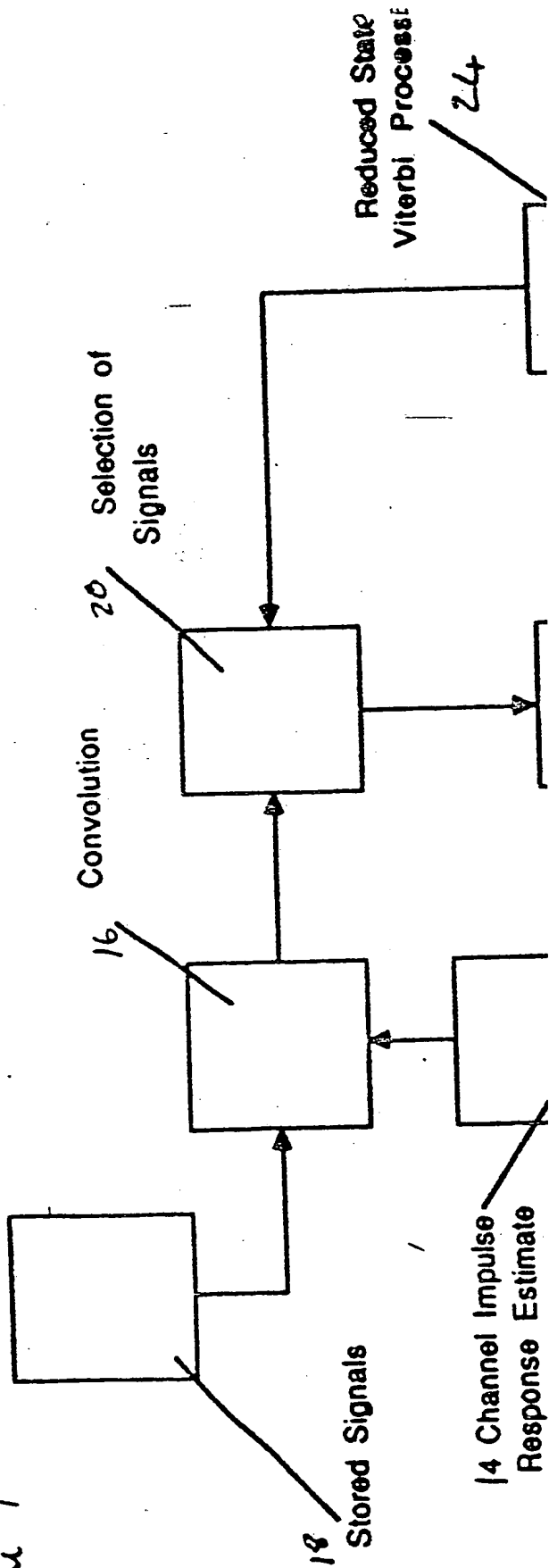
wherein the state path store represents averaged values of all possible 16 states.

g a state path store and means for selecting a sub-set of the stored signal sets with the digital samples, and a processor for processing the generated metrics to determine the most probable value of the received signal.

g an 8-state equaliser.

gally as hereinbefore described with reference to the accompanying drawings.

Figure 1



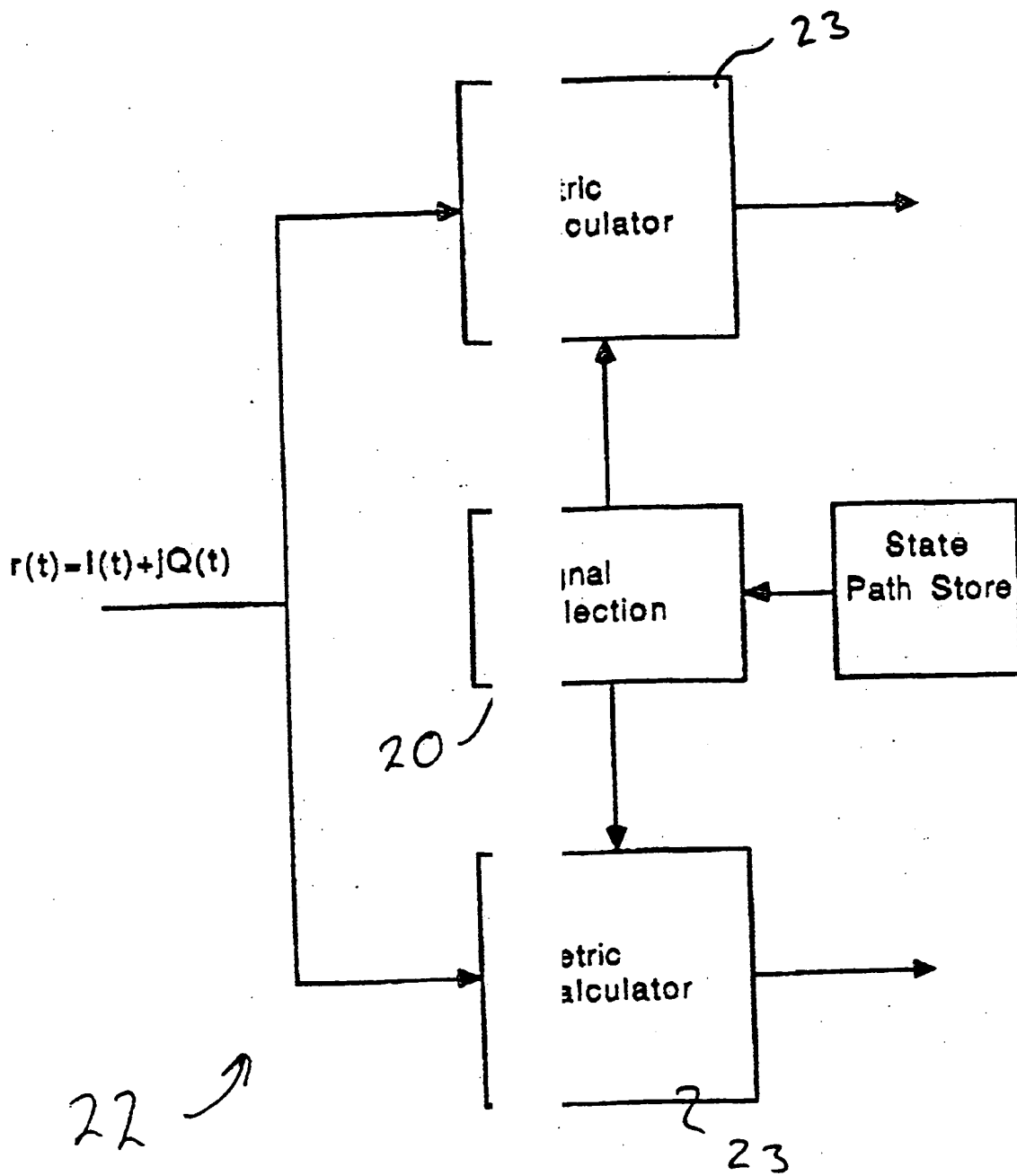


Fig. 2

Metric Generation Element for Reduced State Equaliser.

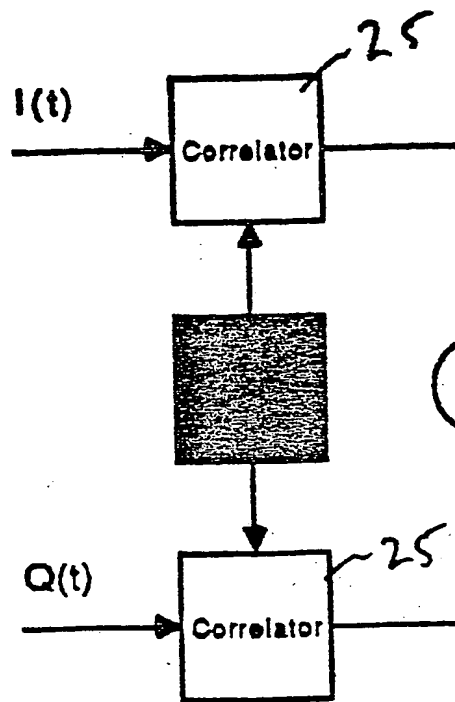
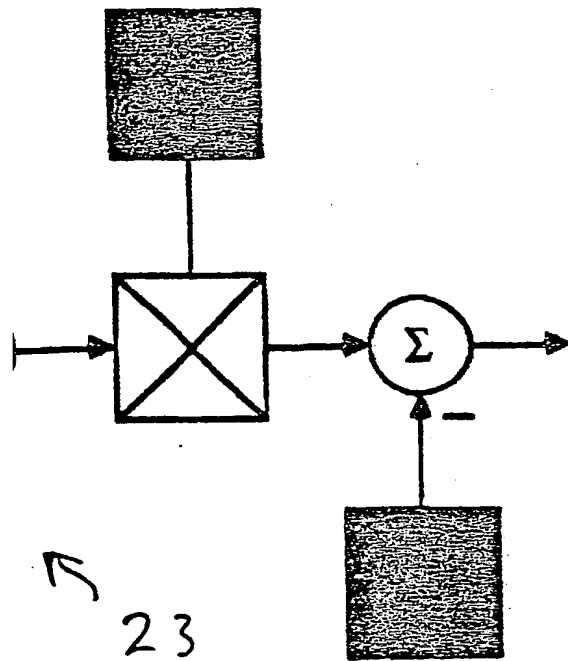


Fig. 3

Metric Calculator



↖

23

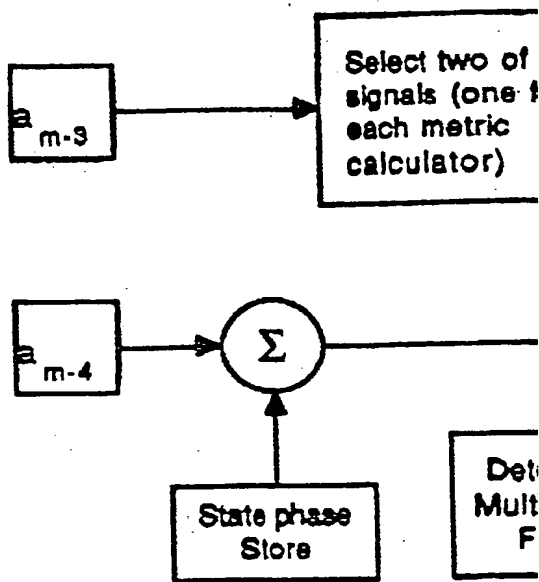
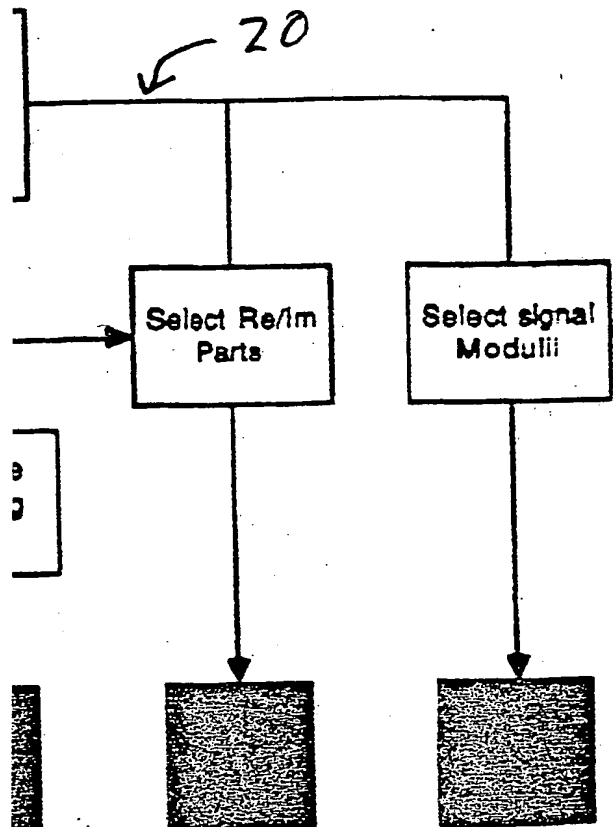
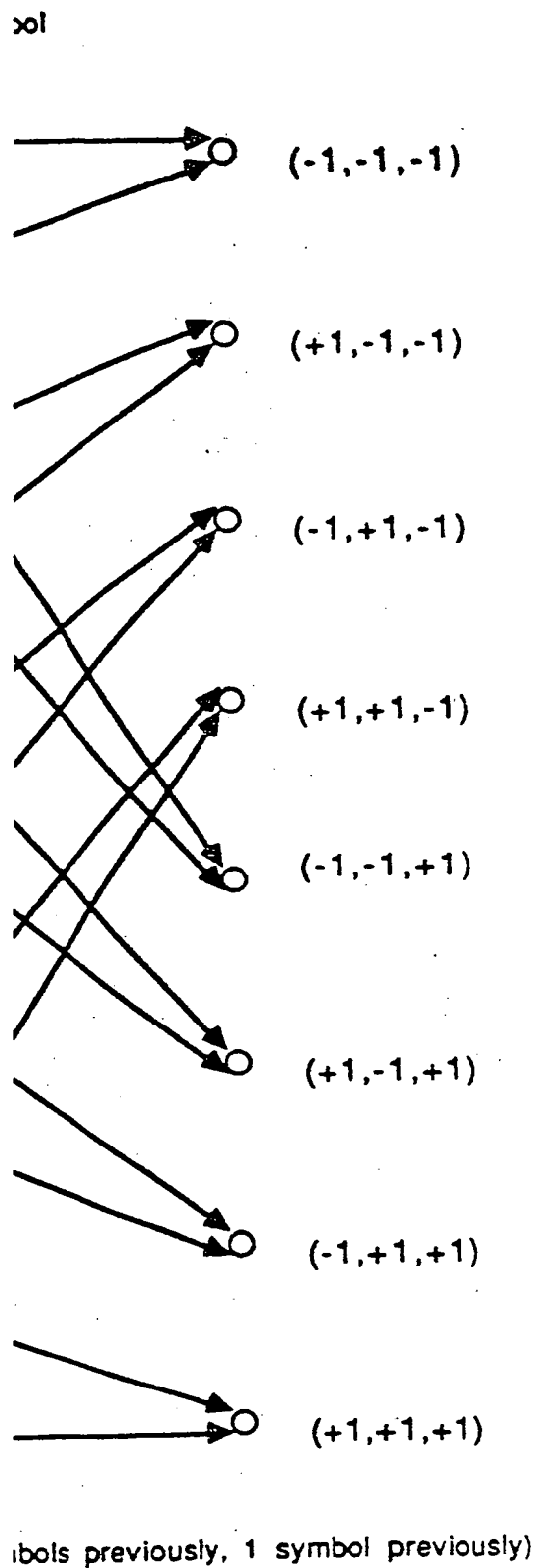
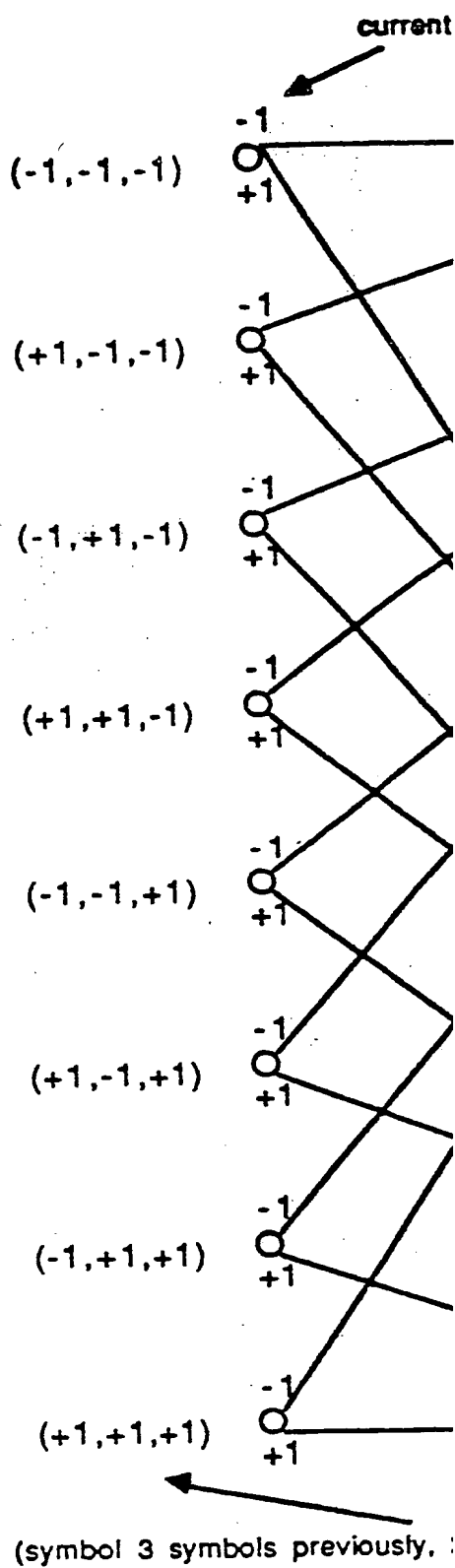


Fig. 4

Signal Selector.





Figur

8 State Trellis Diagram

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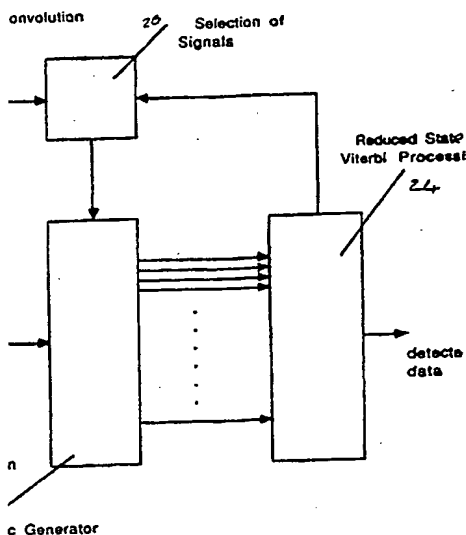
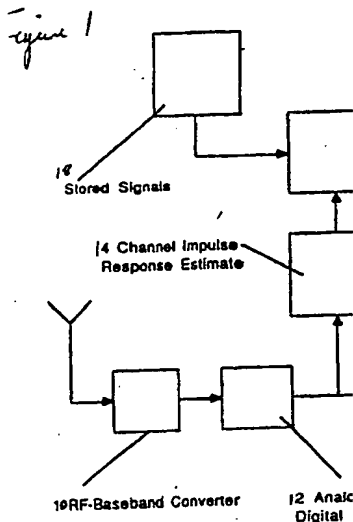
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54 **Equaliser for radio receiver.**

57 A GMSK radio receiver for operation in the presence of strong multipath interference, comprising a baseband frequency converter which operates to produce I and Q baseband GMSK signals, which will be distorted by any multipath interference present, which are correlated with a plurality of

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SEARCH
REPORT

Application Number

EP 89 30 1044

DOCUMENTS CONSIDERED	
Category	Citation of document with indication, where of relevant passages
A	BELL SYSTEM TECHNICAL JOURNAL, November 1973, pages 1541-1562; D.D. FA "Adaptive channel memory truncation for hood sequence estimation" * The whole document *
A	IEEE TRANSACTIONS ON INFORMATIT-19, no. 1, January 1973, pages 120-12 al.: "Adaptive maximum-likelihood sequer digital signaling in the presence of intersy" * The whole document *
A	DE-A-3 246 525 (LICENTIA) * Abstract; figure 1; claim 1; page 6, lines lines 11-26 *
P,X	WO-A-8 809 591 (SINTEF) * Figure 2, abstract; Page 1, lines 22-34; p page 4, line 4; page 5, lines 5-17; claims

The present search report has been drawn up for:

Place of search	Date of
The Hague	05 9

CATEGORY OF CITED DOCUMENTS

X: particularly relevant if taken alone
Y: particularly relevant if combined with another document of the same category
A: technological background
O: non-written disclosure
P: intermediate document
T: theory or principle underlying the invention

E RELEVANT		
Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)	
1-5	H 04 L 25/30	
1-5		
1,2		
1,2		
	TECHNICAL FIELDS SEARCHED (Int. Cl.5)	
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